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relationship between Allegro
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**manager www.orcad.co.uk"OrCAD Allegro PCB Editor
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the new features introduced into
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like High Speed Electrical
constraints scribble routing via
arrays'**

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Manager is one of the Allegro
family shared function for
inputting design rules and
analysis of the rules are satisfied**

or not after component
placement and wiring'

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Constraints In this ?rst example

the requirement is to constrain

nets to a'

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Manager with Design Entry HDL Constraint Manager The scope

of this tutorial does not include details of various"Cadence PCB Editor Parallel Systems

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